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Sheet 1 of 1

Form 1449\*

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INFORMATION DISCLOSURE STATEMENT  
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(Use several sheets if necessary)

Applicant: Aaron M. Schoenfeld et al.

Filing Date: January 5, 2001

Group: Unknown

## U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
C.C.	4,949,361	08/14/1990	Jackson, R.	375	118	06/26/89
C.C.	5,692,165	11/25/1997	Jeddeloh, J., et al.	395	551	09/12/95
C.C.	5,812,462	09/22/1998	Merritt, T.	365	189.05	04/03/97
C.C.	5,819,076	10/06/1998	Jeddeloh, J., et al.	395	552	06/25/97
C.C.	5,852,378	12/22/1998	Keeth, B.	327	171	02/11/97
C.C.	5,857,095	01/05/1999	Jeddeloh, J.M., et al.	395	552	06/25/97
C.C.	5,875,134	02/23/1999	Cloud, E.H.	365	193	09/08/97
C.C.	5,886,948	03/23/1999	Ryan, K.J.	365	233	10/29/97
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C.C.	6,026,051	02/15/2000	Keeth, B., et al.	365	233	03/24/99
C.C.	6,069,506	05/30/2000	Miller, Jr., J., et al.	327	156	05/20/98
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C.C.	6,137,334	10/24/2000	Miller, Jr., J., et al.	327	271	07/06/98
C.C.	6,243,797	06/05/2001	Merritt, T.	711	167	02/18/97

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**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
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## OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial	
C.C.	"DDR SDRAM Functionality and Controller Read Data Capture", <u>DesignLine</u> , Volume 8, Issue 3, 3Q99, Micron Technology, Inc., pp. 1-24, (1999)
C.C.	"Double Data Rate (DDR) Sdram", <u>Micron Semiconductor Products, Inc.</u> , 64Mb: x4, x8, x16 DDR SDRAM, pp. 1-66, (2000)

Examiner

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Date Considered

3/12/04

\*Substitute Disclosure Statement Form (PTO-1449)

\*\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.